

FIGURE 1

100a

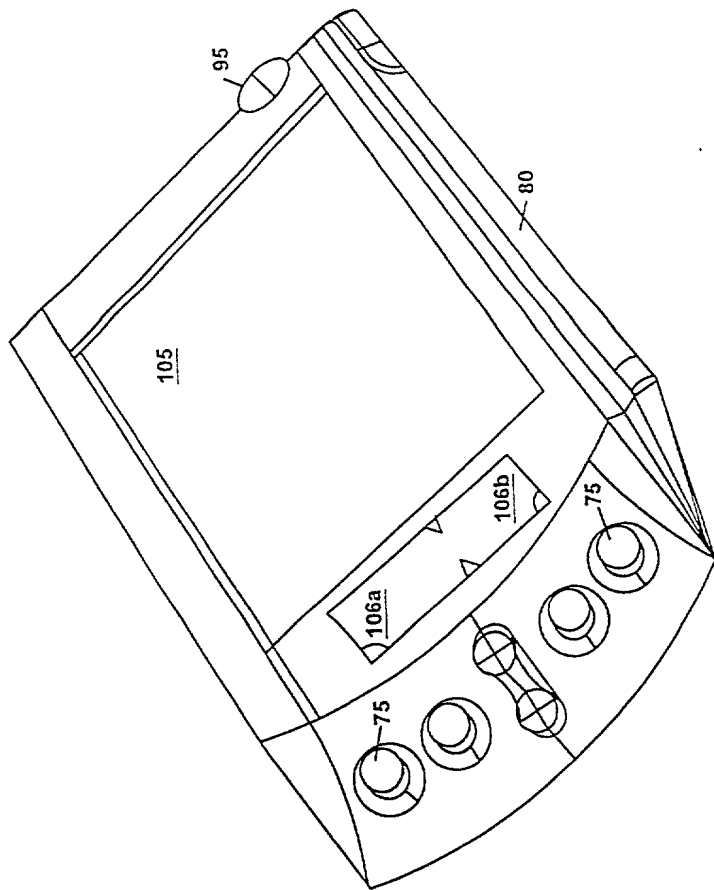


FIGURE 2

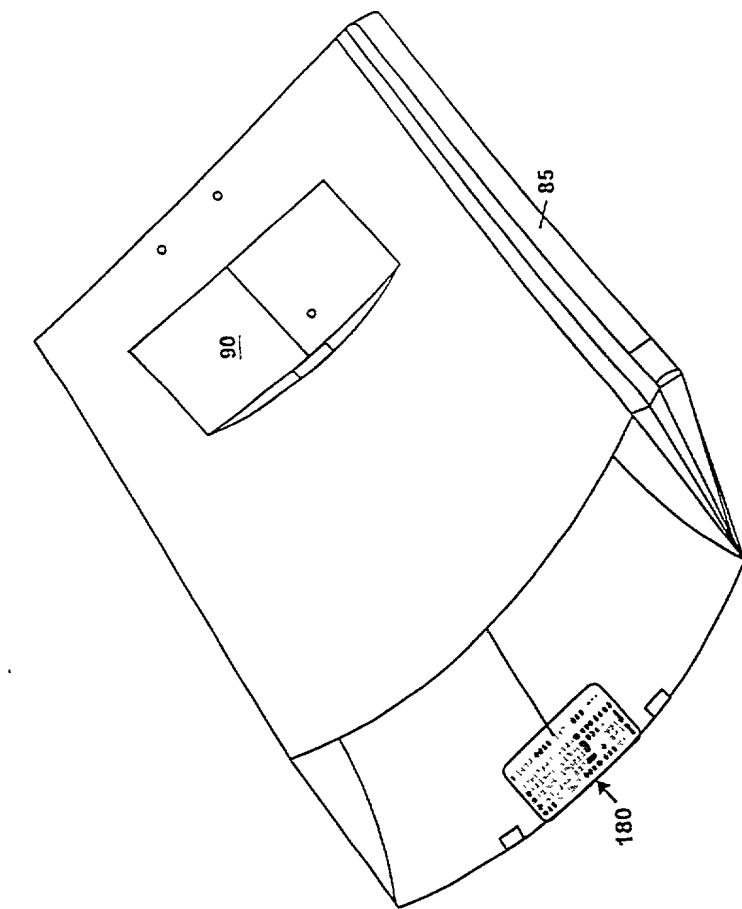


FIGURE 3

100

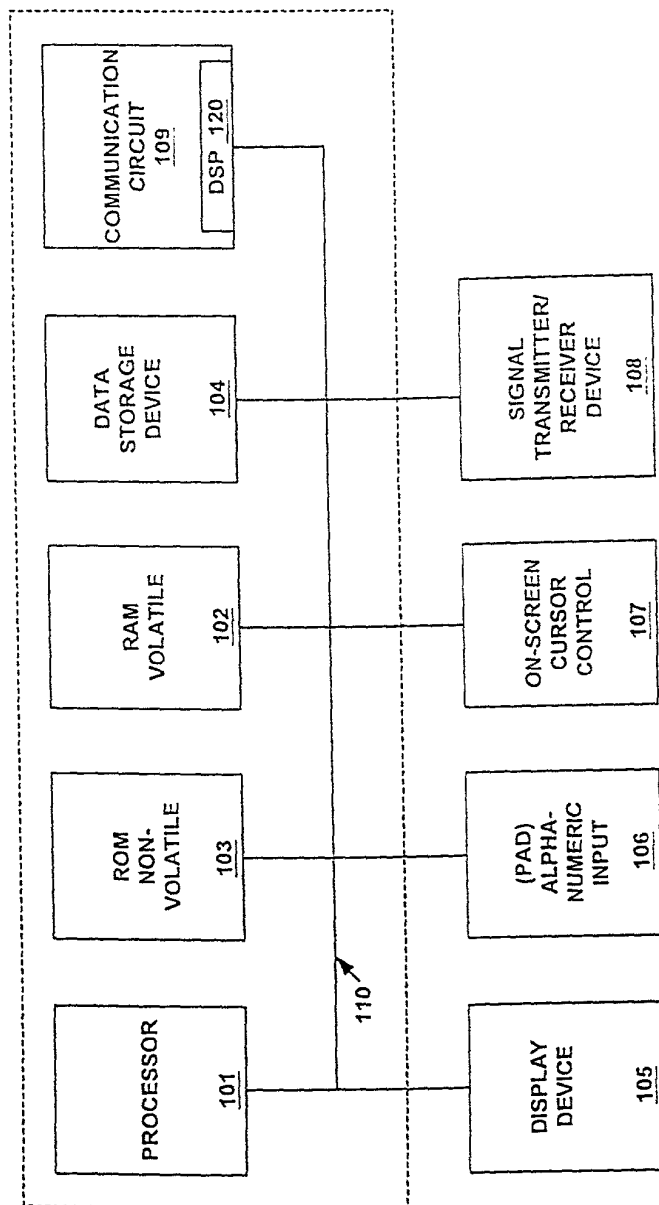


FIGURE 4

<u>500A</u>		MOBITEX Frame Header							
		7	6	5	4	3	2	1	0
Byte 0	Bit Synch 1								<u>501</u>
Byte 1	Bit Synch 2								<u>502</u>
Byte 2	Frame Synch 1								<u>503</u>
Byte 3	Frame Synch 2								<u>504</u>
Byte 4	Control Byte 1								<u>505A</u>
Byte 5	Control Byte 2								<u>506A</u>
Byte 6	FEC 1 (P)				<u>507</u>	FEC 2 (Q)			<u>508</u>

Fig. 5A

500B

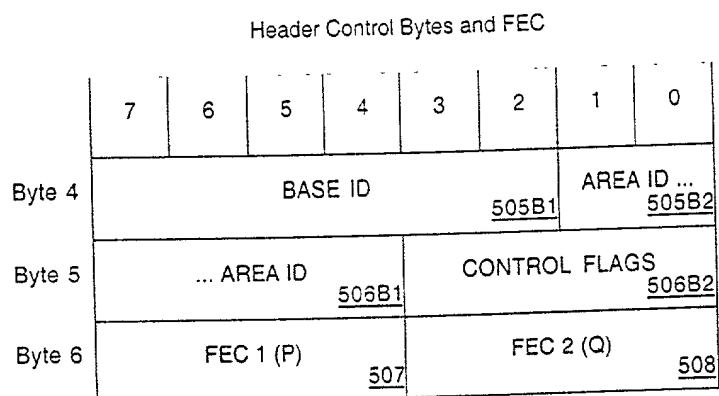


Fig. 5B

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600

A5	C0	C1	C2	C3	P0	P3	Q0	Q1	Q2	Q3
1	0	0	0	0	1	1	1	0	0	1

Fig. 6A

600

A5	C0	C1	C2	C3	P0	P3	Q0	Q1	Q2	Q3
1	0	0	0	0	1	1	1	0	0	1

Fig. 6B

600

A5	C0	C1	C2	C3	P0	P3	Q0	Q1	Q2	Q3
1	0	0	0	0	1	1	1	0	0	1

Fig. 6C

600

A5	C0	C1	C2	C3	P0	P3	Q0	Q1	Q2	Q3
1	0	0	0	0	1	1	1	0	0	1

Fig. 6D

600

A5	C0	C1	C2	C3	P0	P3	Q0	Q1	Q2	Q3
1	0	0	0	0	1	1	1	0	0	1

Fig. 6E

700

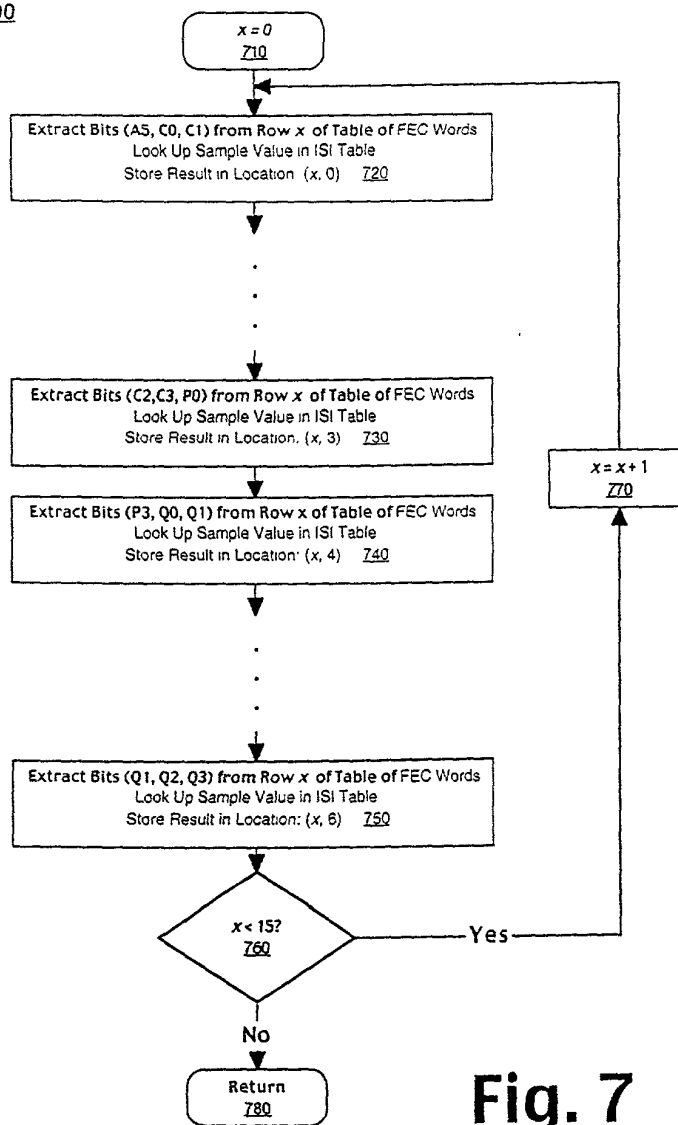


Fig. 7

800

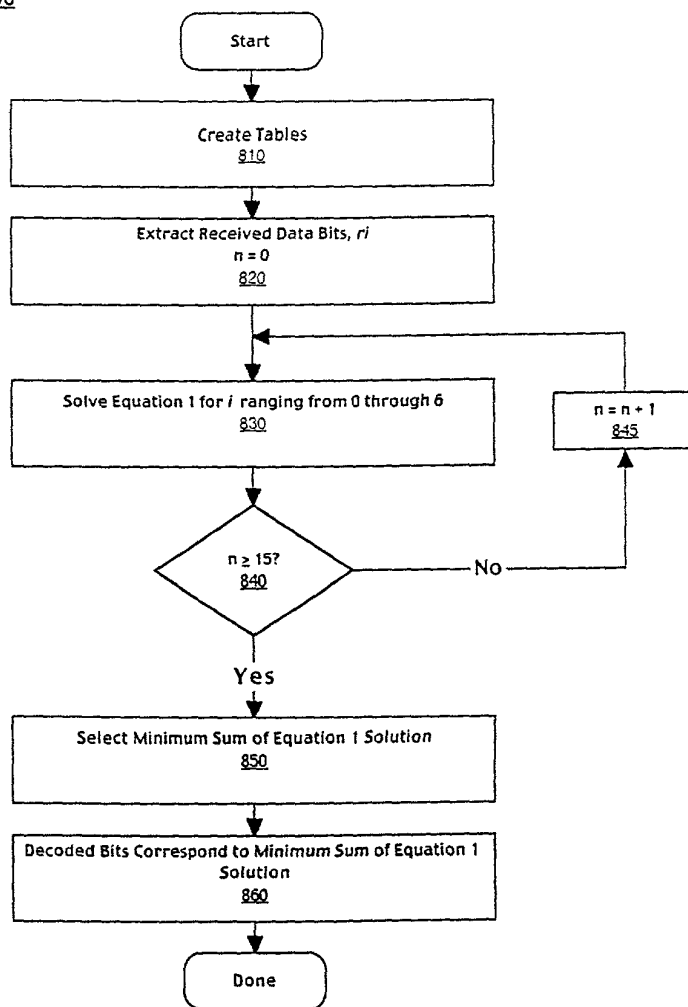


Fig. 8